

### Precision Timing for Fixed Wireless Access (FWA)

5G FWA (fixed wireless access) provides ultra-high bandwidth to deliver heavy content at significantly faster speeds and at a lower cost to fiber and other fixed internet lines. While a seamless shift from traditional networks to 5G front and back haul is difficult to achieve due to the large existing investment by the telecom operators in their 4G/LTE infrastructure, some companies are choosing to integrate 5G FWA fronthaul equipment with their 4G/LTE backhaul for a faster rollout and improved ROI.

### Key Considerations

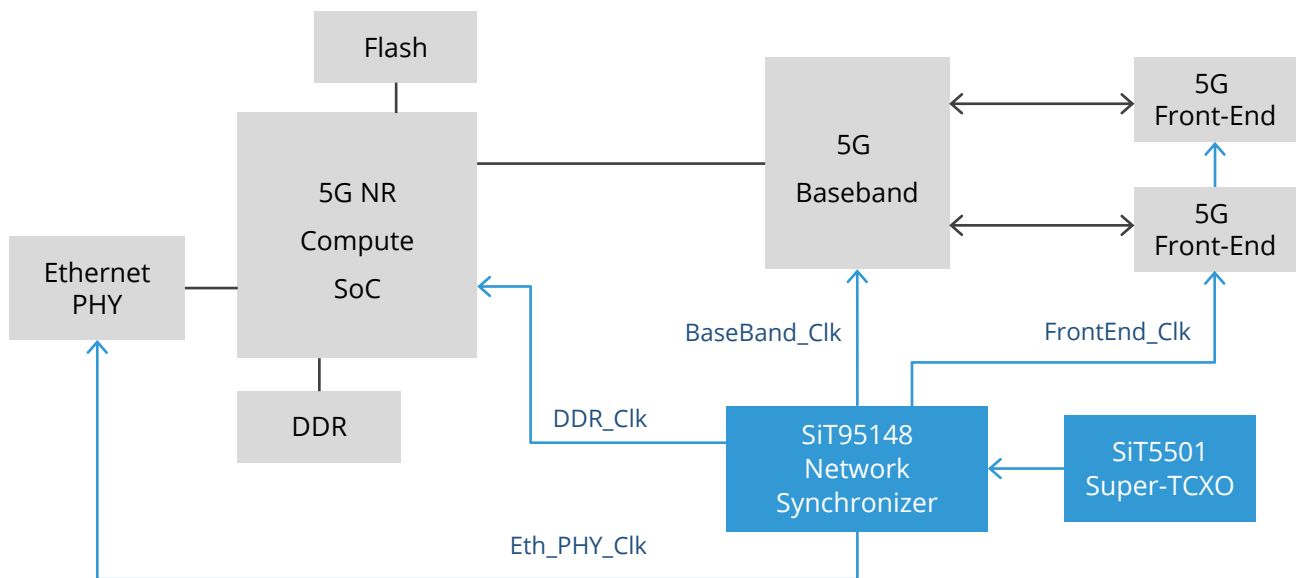
- Wide temperature range
- EMI resilience
- Temperature stability
- Low phase noise

The preferred mmWave band being deployed by most carriers is sub-6GHz or the C-band, specifically 3.3 GHz to 4.2 GHz range as it covers a radius of more than 5 km and supports a maximum throughput of 5 Gbps.

Key reasons for 5G FWA growth are:

- Reduced connectivity costs: wireless infrastructure is lower cost than fixed wired infrastructure
- Faster speeds: 300 Mbps internet access is being touted with the expectation of reaching 1 Gbps
- Lower latency: 5G offers very low latency making it ideal for environments requiring dependable connectivity
- Lower energy use: 5G uses less energy to connect and transmit than alternative options

### FWA Block Diagram



Featured products – please refer to [SiTime.com](https://www.sitime.com) or [contact us](#) for more options.

Type	Product	Frequency	Key Features	Key Values
Network Synchronizer	<a href="#">SiT95148</a>	1 to 220 MHz	<ul style="list-style-type: none"> <li>• 4 inputs, 11 outputs</li> <li>• Up to 2 GHz clock output frequencies</li> <li>• 120 fs integrated phase jitter<sup>1</sup></li> <li>• Programmable PLL loop bandwidth, 1 MHz to 4 KHz</li> <li>• Digital frequency control</li> <li>• -40°C to 85°C</li> <li>• 9.0 x 9.0 mm package</li> </ul>	<ul style="list-style-type: none"> <li>• Multiple clock domains, multiple clock outputs enables complex clock architectures</li> <li>• 10x more resistant to vibration and board bending</li> </ul>
Super-TCXO	<a href="#">SiT5501</a> <sup>2</sup>	1 to 60 MHz	<ul style="list-style-type: none"> <li>• ±10 ppb stability</li> <li>• ±0.5 ppb/°C</li> <li>• 2x10<sup>-11</sup> Allan deviation</li> <li>• -40°C to 105°C</li> <li>• 7.0 x 5.0 mm package</li> </ul>	<ul style="list-style-type: none"> <li>• Ensures QoS requirements are met in Telecom Equipment in hostile environments</li> </ul>
Differential Oscillator	<a href="#">SiT9375</a>	25 to 644.5 MHz, 70 fs Integrated Phase Jitter <sup>1</sup>	<ul style="list-style-type: none"> <li>• ±20 ppm to ±50 ppm frequency stability</li> <li>• LVPECL, LVDS, HCSL</li> <li>• 1.8 V to 3.3 V</li> </ul>	<ul style="list-style-type: none"> <li>• Meets demanding jitter requirements</li> <li>• Small PCB footprint, easier layout</li> <li>• Easy design due to flexibility</li> <li>• MEMS reliability</li> </ul>
	<a href="#">SiT9501</a>	25 to 644.5 MHz, 150 fs Integrated Phase Jitter <sup>1</sup>	<ul style="list-style-type: none"> <li>• -40°C to 105°C</li> <li>• 2.0 x 1.6 mm, 2.5 x 2.0 mm, 3.2 x 2.5 mm packages</li> </ul>	
Clock Generator	<a href="#">SiT91211</a> <sup>3</sup>	1 to 750 MHz, 200 fs Integrated Phase Jitter <sup>1</sup>	<ul style="list-style-type: none"> <li>• 4 differential output clocks</li> <li>• ±20 ppm frequency stability</li> <li>• LVDS, LVPECL, LPHCSL</li> </ul>	<ul style="list-style-type: none"> <li>• Simplifies clock tree design with multiple low jitter clocks</li> <li>• Programmable clocks add flexibility to complex clocking architectures</li> <li>• Better frequency stability and noise immunity in harsh environments</li> <li>• Small PCB footprint, compact layout</li> </ul>
	<a href="#">SiT91213</a> <sup>3</sup>	1 to 750 MHz, 90 fs Integrated Phase Jitter <sup>1</sup>	<ul style="list-style-type: none"> <li>• 0.01 ps/mV PSRR</li> <li>• -40°C to 105°C</li> <li>• 4 mm x 4 mm package</li> </ul>	

<sup>1</sup> 12 kHz to 20 MHz integration range; <sup>2</sup> [Contact SiTime](#) for higher frequencies. <sup>3</sup> [Contact SiTime](#) for availability.

### SiTime advantages:

SiTime devices offer the following advantages over quartz crystals, which are particularly important for telecom applications:

- SiT9514x family of synchronizer devices offer a complete synchronization clock tree on a chip. No external crystal is required.
- $dF/dT$ , the effect of temperature variation on frequency stability, is 4x better than crystal-based TCXOs. This ensures better quality of service under airflow, heating, and cooling conditions.
- Silicon MEMS TCXOs offer similar stability as crystal-based OCXOs, at smaller form factor and lower power
- SiTime oscillators are factory programmable to any frequency.
- Silicon MEMS-based timing devices have a 30x higher reliability than quartz.
- No activity dip or cold start issues.



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