



Timing Enables the Future of Optical Modules, Datacom

July 2020



Lowest Jitter 70 fs, Smallest by 35%

MEMS Oscillators

for Optical Modules and Data Communications



SiTime Timing Makes 5G Vision a Reality

10x
Faster

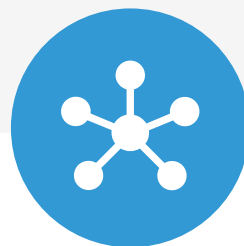


Better jitter,
phase noise



10,000x
More Traffic

10-100x
More Devices



Better environmental
resilience



50x
Lower Latency

Zero
Perceived Downtime

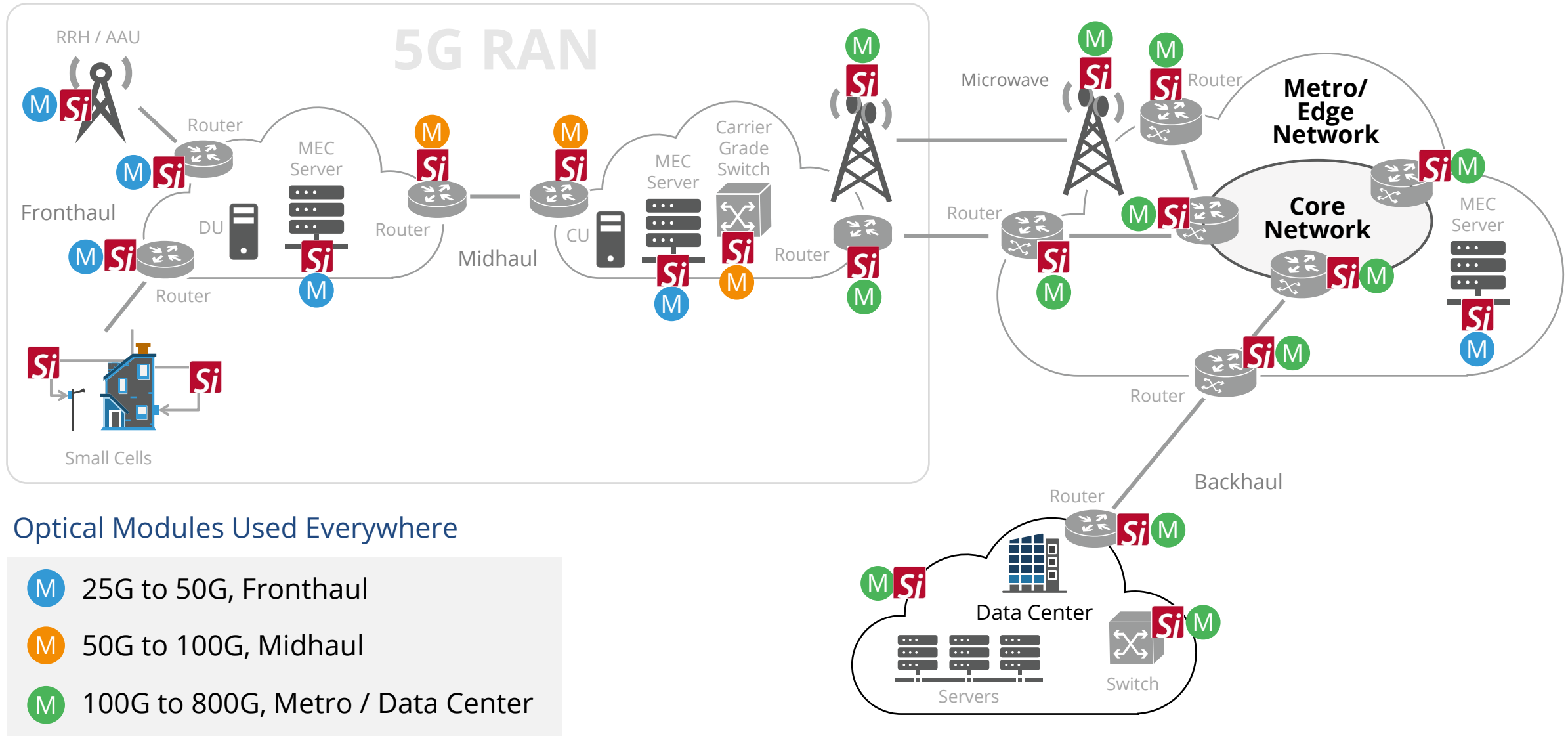


Higher reliability,
lower power



90%
Less Energy

SiTime Empowers 5G Network Connectivity

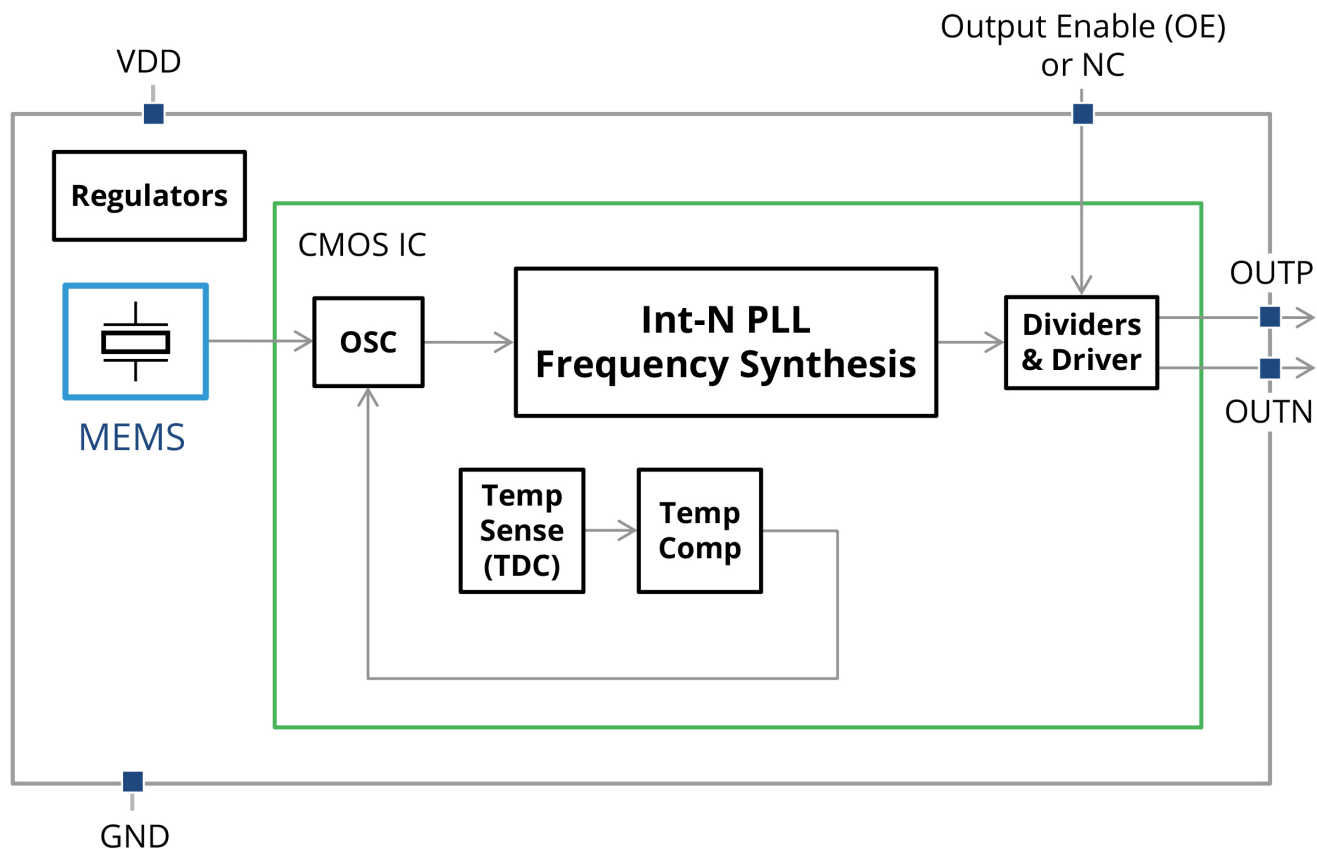


SiTime Solves Timing Problems in 5G Infrastructure

Network Devices	5G Requirement	SiTime Value	SiTime Products
Optical Modules	<ul style="list-style-type: none">• 4x faster• Less power/bit• Denser designs	<ul style="list-style-type: none">• Lowest-jitter• 2x more robust to supply noise• 50% smaller	SiT9501 XO
Switches, Routers, Radios	<ul style="list-style-type: none">• 10x tighter time synchronization• Outdoor deployment• Higher reliability	<ul style="list-style-type: none">• 4x better dF/dT• 20x better g-sensitivity, 105 °C• 40x better MTBF	Elite TCXO Emerald OCXO

Note: dF/dT measures the change in frequency with changes in temperature

State-of-the-art MEMS, PLL Deliver Ultra-low Jitter, Eliminates Spurs



Integrated MEMS Resonator

- Eliminates quartz related issues
- 40x better MTBF reliability
- 20x better g -sensitivity

Ultra-low Noise Analog

- 25 MHz to 644.53125 MHz
- 0.01 ps/mv best-in-class PSNR
- Ultra-low jitter with no spurs

SiT9501 – Lowest-jitter Programmable XO Engineered for Optical Modules

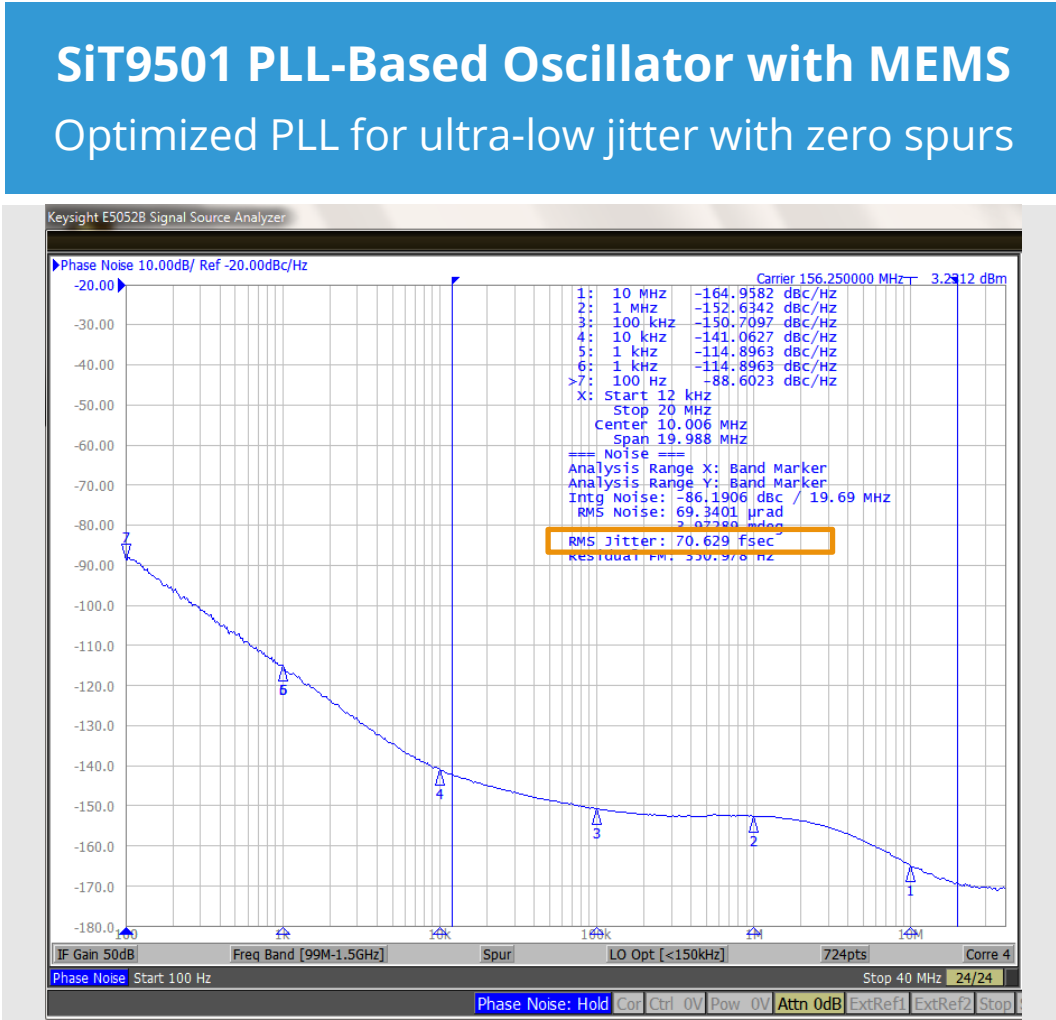
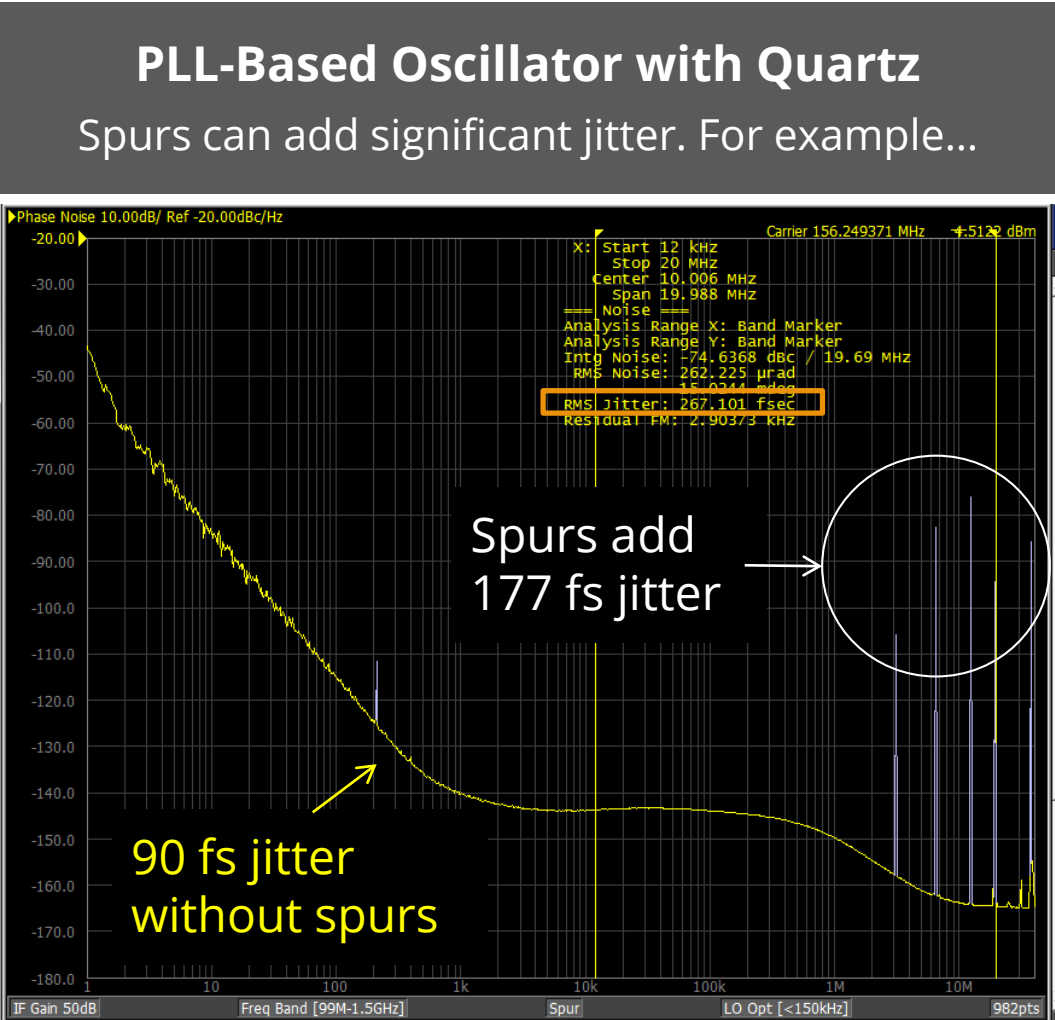
- 70 fs – **Lowest jitter** improves BER
- 2.0x1.6 mm – **35% smaller** speeds development
- 0.01 ps/mV **best-in-class PSNR** improves BER
- 43 mA **best-in-class LVPECL power** reduces operating costs
- FlexSwing – **Programmable swing** enables low-power designs



Other common form factors:

- QSFP28, QSFP+, QSFP-DD
- CFP, CFP2, CFP4, CPAK
- OSFP

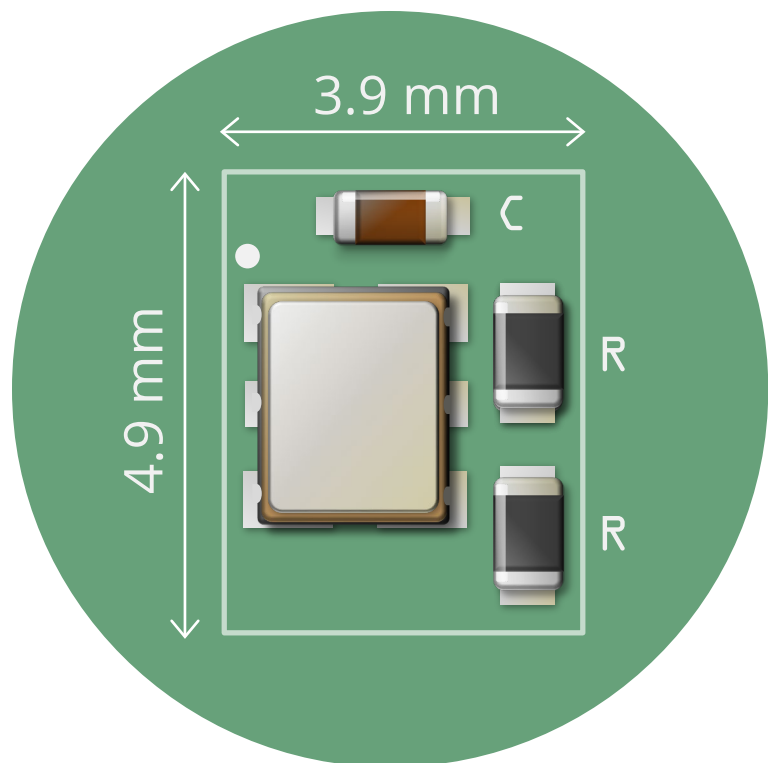
SiT9501 – A New Paradigm for MEMS Performance: 70 fs Jitter, No Spurs



SiT9501 Wins Against Quartz Solutions

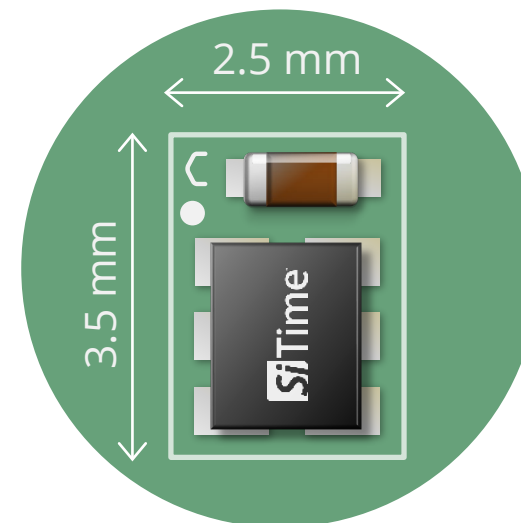
Attribute	XO non-PLL based	XO PLL-based	SiT9501 PLL-based
Resonator technology	Quartz	Quartz	SiTime MEMS
No phase noise spurs	Yes	No	Yes
Resistant to shock, vibration	No	No	Yes 10x Better
Resistant to supply noise	No	Yes	Yes
Smallest package size (mm)	2.5 x 2.0	2.5 x 2.0	2.0 x 1.6 Save 35% area
Reliability	Good	Good	Best
Programmable swing	No	No	Yes – FlexSwing
Integrated LVPECL bias resistor	No	No	Yes – FlexSwing Save 50% area
Programmability	No	No	Yes

Smallest Differential Oscillator Solution



Quartz 2.5 x 2.0 mm
plus LVPECL source-bias resistors

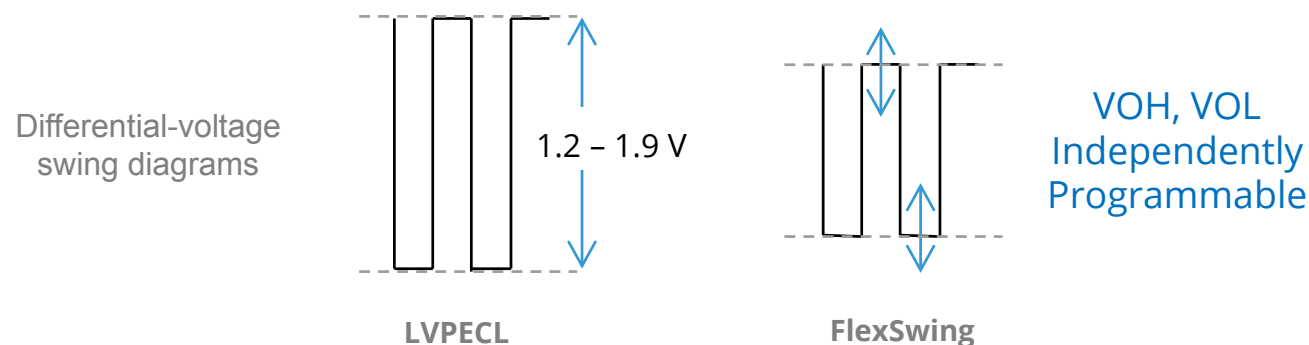
50%
SMALLER
FOOTPRINT



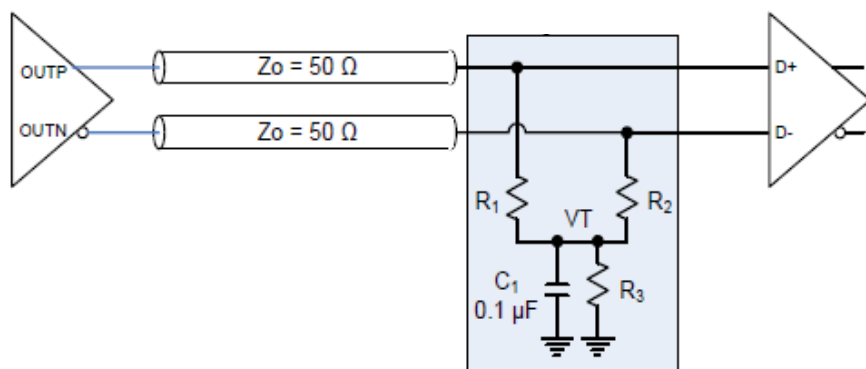
SiT9501 2.0 x 1.6 mm with integrated
LVPECL source-bias resistors

SiT9501 FlexSwing Driver Reduces System Power

- Output levels are programmable to enable compliance with low-voltage chipsets



- 28% less power using 1.8 V supply compared to 2.5 V
- 3.5x lower load current with optimum termination



Swing Option	Termination (Ohm)	Load Current (mA, typ)
LVPECL , $V_{\text{swing}} = 1.6 \text{ V}$ <ul style="list-style-type: none"> $V_H = V_{DD} - 0.9 \text{ V}$ $V_L = V_{DD} - 1.7 \text{ V}$ 	$R_3 = 50$	28
FlexSwing , $V_{\text{swing}} = 1 \text{ V}^*$ <ul style="list-style-type: none"> $V_H = V_{DD} - 1.8 \text{ V}$ $V_L = V_{DD} - 2.3 \text{ V}$ 	$R_3 = 220$	7.5

* FlexSwing order code "WB"

SiTime – “One Stop Shop” for Differential XO’s

Stability

±10 ppm
±20 ppm
±25 ppm
±50 ppm

Phase Jitter

70 fs rms
200 fs rms
230 fs rms
600 fs rms

Package

2 x 1.6 mm
2.5 x 2 mm
3.2 x 2.5 mm
5 x 3.2 mm
7 x 5 mm

Signal

LVPECL
LVDS
HCSL
LP-HCSL
FlexSwing

Voltage

1.8 – 3.3 V
2.5 – 3.3 V
1.8 V
2.5 V
3.3 V

Temp

-20 to 70°C
-40 to 85°C
-40 to 105°C

Other Features

0.001 – 1 GHz
SSXO for EMI
PCIe Gen 1-5

SiTime MEMS Differential Oscillator Portfolio

13 Year Ship History to Comms, Enterprise, Cloud

OPTICAL MODULES



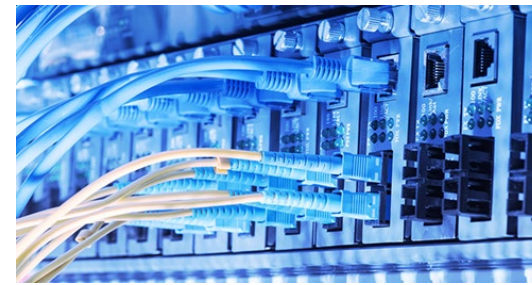
Small size, Low Power

DATA CENTERS



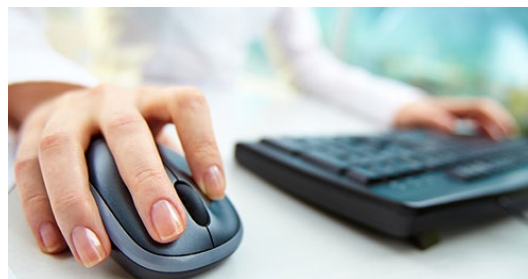
Low Jitter, Integrated LDO

10G/100G/400G COMM



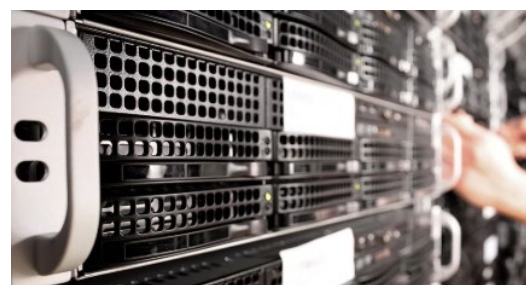
Resilient to Supply Noise

COMPUTING



Low EMI Emission

SERVERS, STORAGE



Short Lead Time