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|  | Title: | Performance Report SiT2018B, 100MHz | | | |
| | Type: | Performance report | Rev: | 1.0 | |
| | Orig: | | Date: | Nov 24, 2014 | |

This report contains sample performance data for SiT2018B-100MHz.

Conditions:

- Frequency 100 MHz
- Vdd 1.8V, 2.5V, 2.8V, 3.0V, 3.3V
- Temperature 25°C
- Termination:
 - o No load for IDD
 - o 50Ω to GND for phase noise
 - o 15pF for other tests

Equipment:


- Agilent DSA90604 oscilloscope (6GHz, 20Gsps)
 - o Period jitter, waveform, rise/fall time, duty cycle, amplitude
- Agilent E5052B Signal Source Analyzer
 - o Phase noise, integrated phase jitter
- Power supply current
 - o Agilent 34401A DMM

Data:

- Random Phase jitter, Period Jitter, Duty cycle, Rise/Fall time, Amplitude, Idd
- Output waveforms
- Frequency stability versus temperature

Table 1. Performance data

| Parameter | Units | Voltage | | | | |
|--|-----------|---------|-------|-------|-------|-------|
| | | 1.8 V | 2.5 V | 2.8 V | 3.0 V | 3.3 V |
| Random Phase jitter (900kHz - 20MHz) | ps, rms | 0.62 | 0.73 | 0.75 | 0.75 | 0.75 |
| Random Phase jitter (12kHz - 20MHz) | ps, rms | 1.49 | 1.62 | 1.65 | 1.65 | 1.62 |
| Period jitter | ps, rms | 2.40 | 1.81 | 1.69 | 1.72 | 1.68 |
| Period jitter (10,000 cycles) | ps, pk-pk | 15.8 | 12.6 | 12.3 | 11.8 | 11.6 |
| Duty cycle | % | 49.6 | 49.7 | 50.4 | 51.1 | 51.7 |
| Rise time (20% - 80%) | ns | 1.22 | 1.01 | 0.92 | 0.98 | 0.91 |
| Fall time (80% - 20%) | ns | 1.24 | 0.98 | 0.90 | 0.97 | 0.92 |
| Amplitude | V | 1.79 | 2.50 | 2.81 | 3.04 | 3.32 |
| Current consumption (no load, output enabled) | mA | 4.59 | 4.99 | 5.19 | 5.26 | 5.48 |
| Current consumption (no load, output disabled) | mA | 3.53 | 3.60 | 3.65 | 3.69 | 3.76 |

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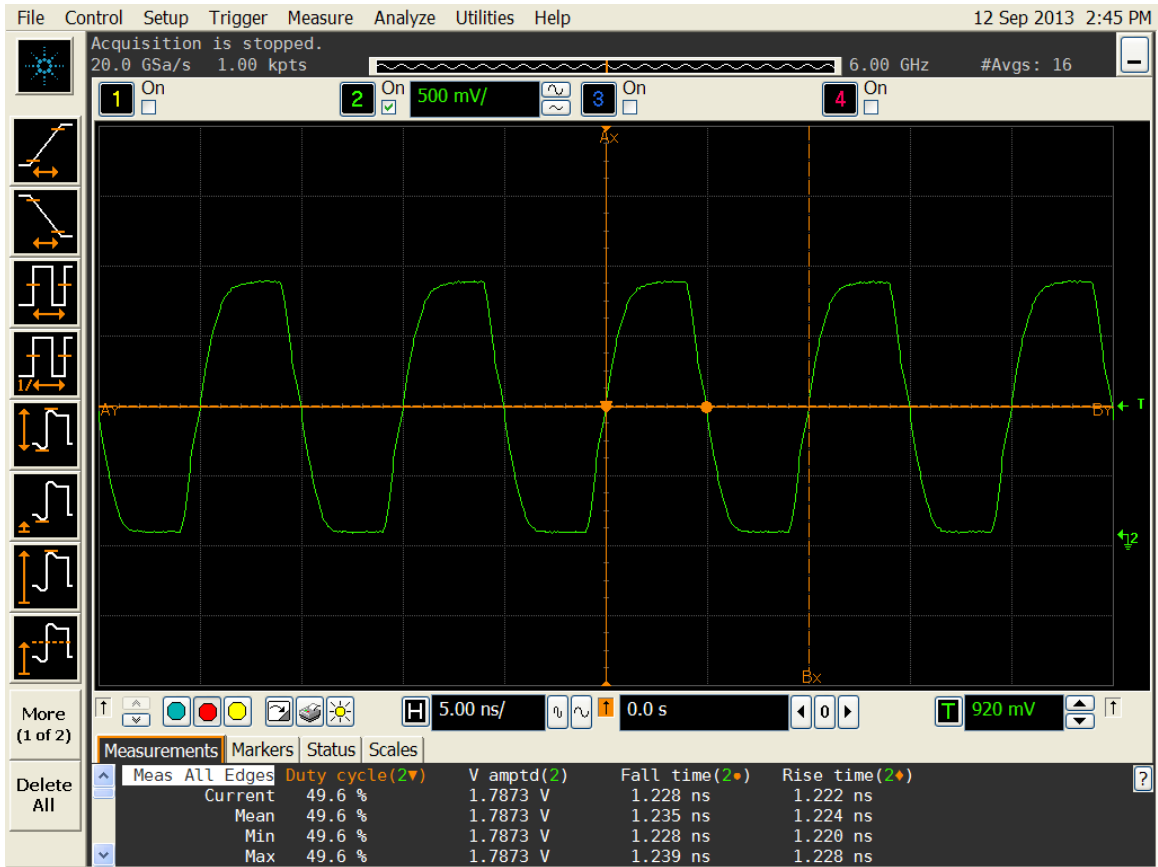



Figure 1. Duty cycle, Rise/Fall time and Amplitude 1.8V

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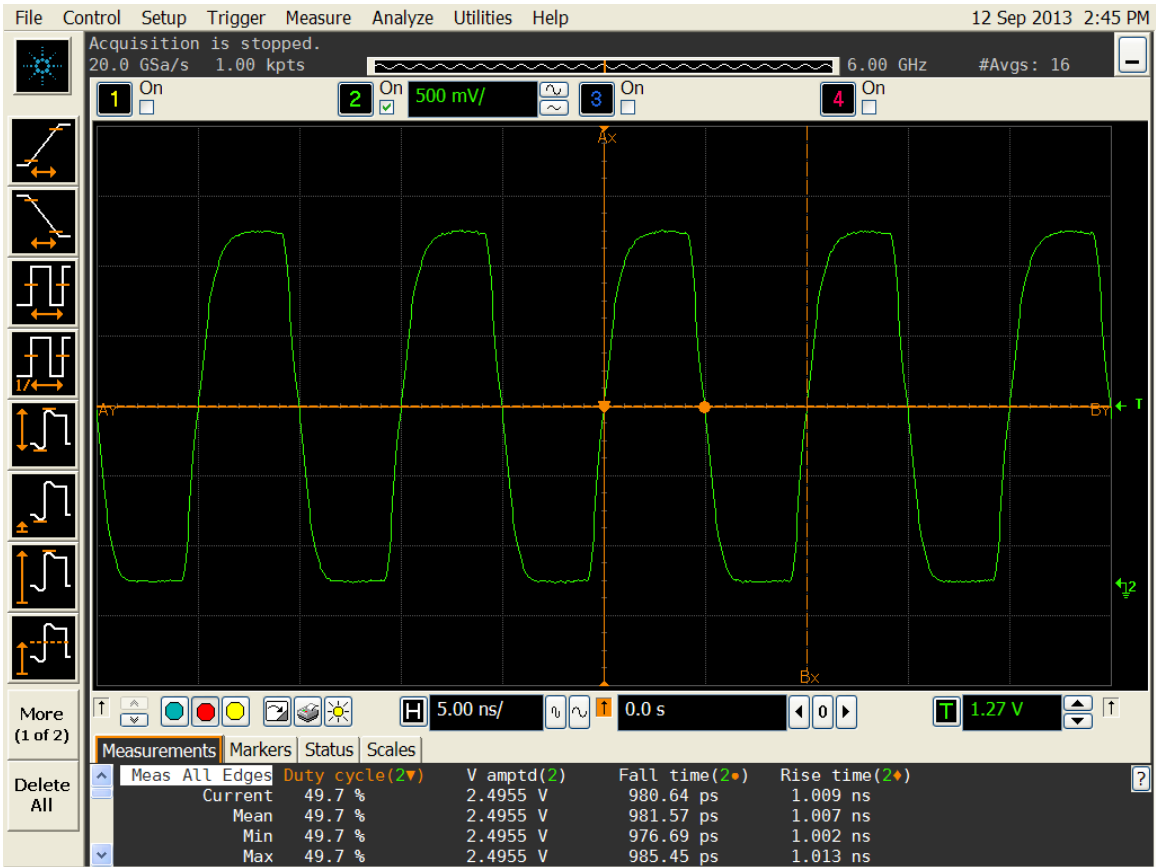



Figure 2. Duty cycle, Rise/Fall time and Amplitude 2.5V

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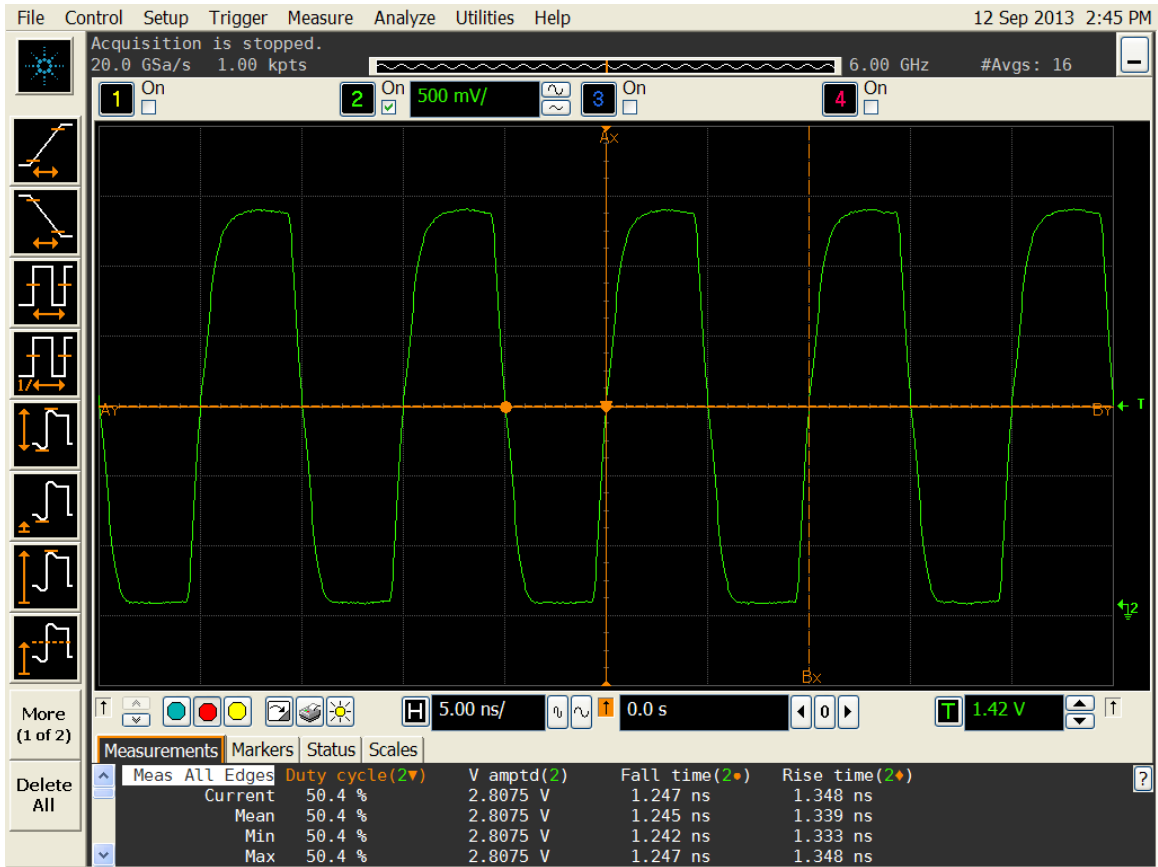



Figure 3. Duty cycle, Rise/Fall time and Amplitude 2.8V

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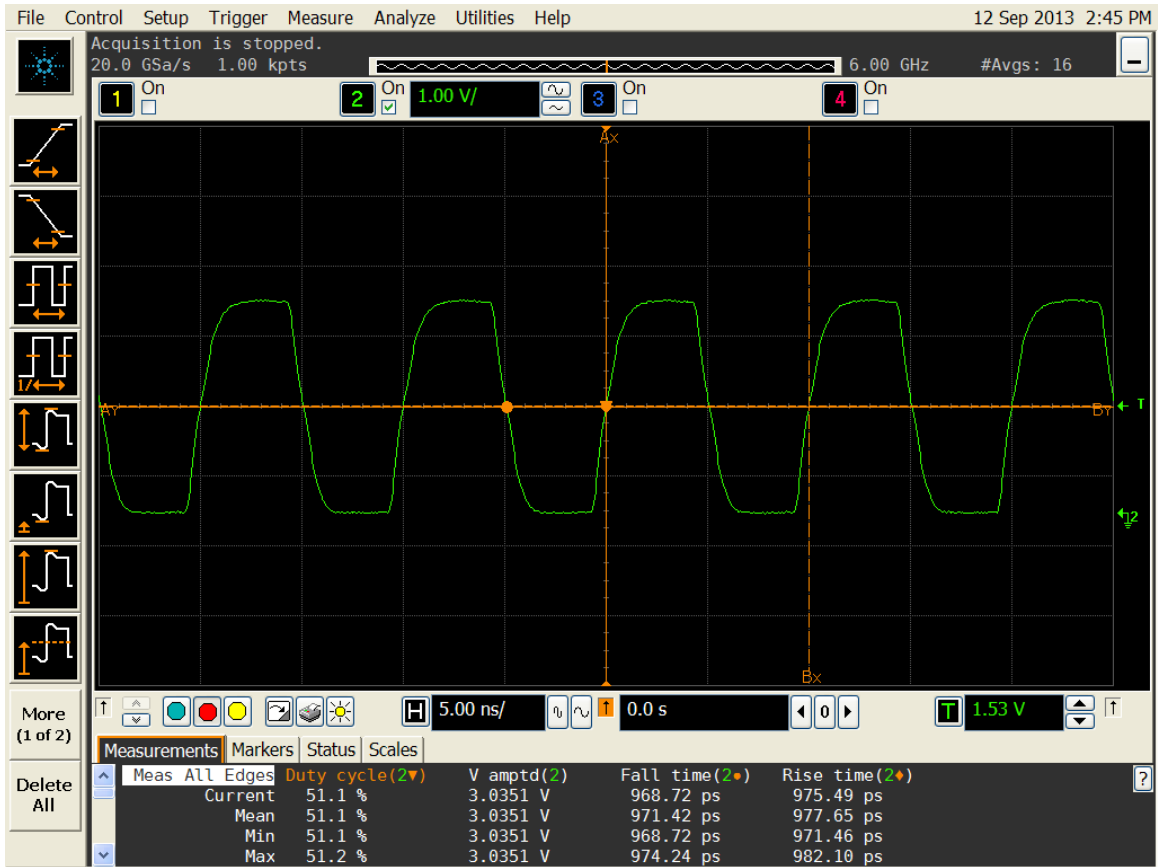



Figure 4. Duty cycle, Rise/Fall time and Amplitude 3.0V

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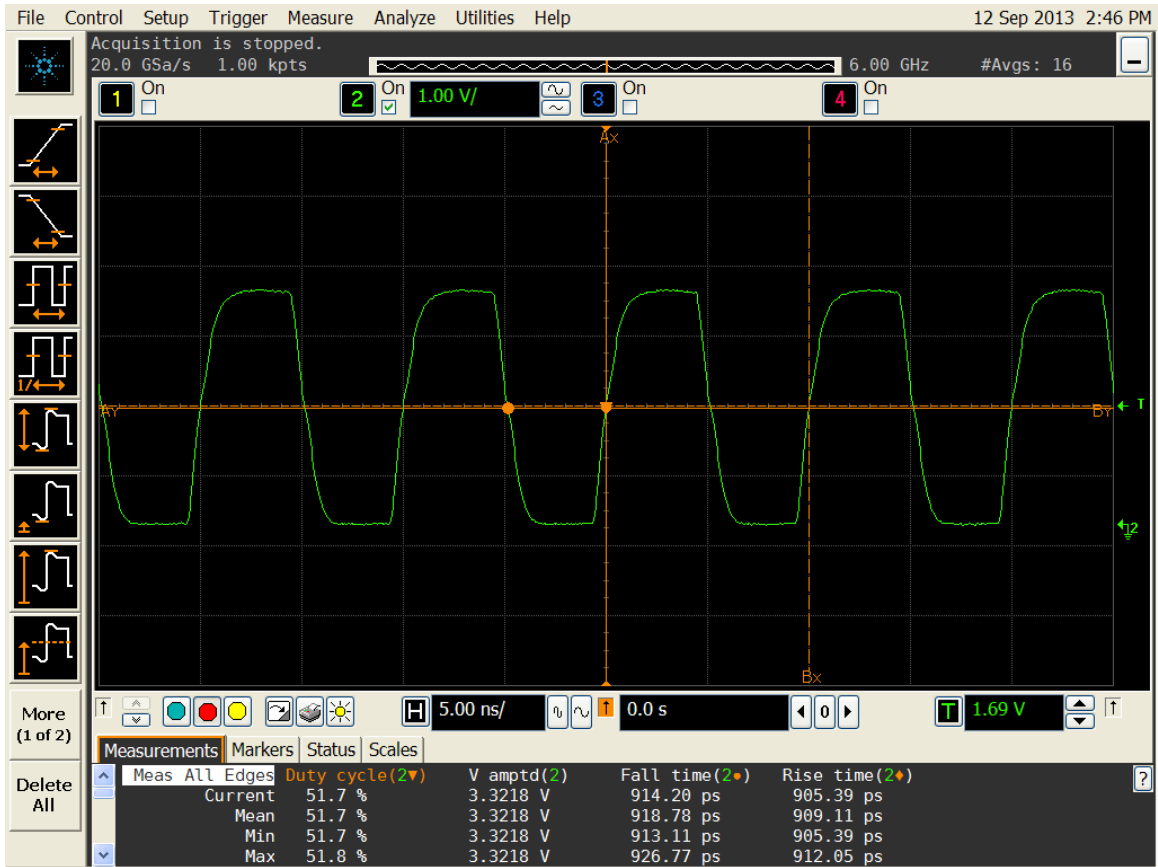


Figure 5. Duty cycle, Rise/Fall time and Amplitude 3.3V

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Figure 6. Frequency stability* versus temperature

*Please note that frequency stability in SiTime devices is not depended on output frequency.