

**SiTime MEMS Timing Benefits**

**Precision Timing**

- 50x better acceleration sensitivity
- $\pm 0.5$  ppm up to 105°C
- No activity dips

**Most Robust in Harsh Conditions**

- 20x better mechanical shock survivability
- 4x better vibration resistance
- Extended operating temperature range

**Higher Reliability**

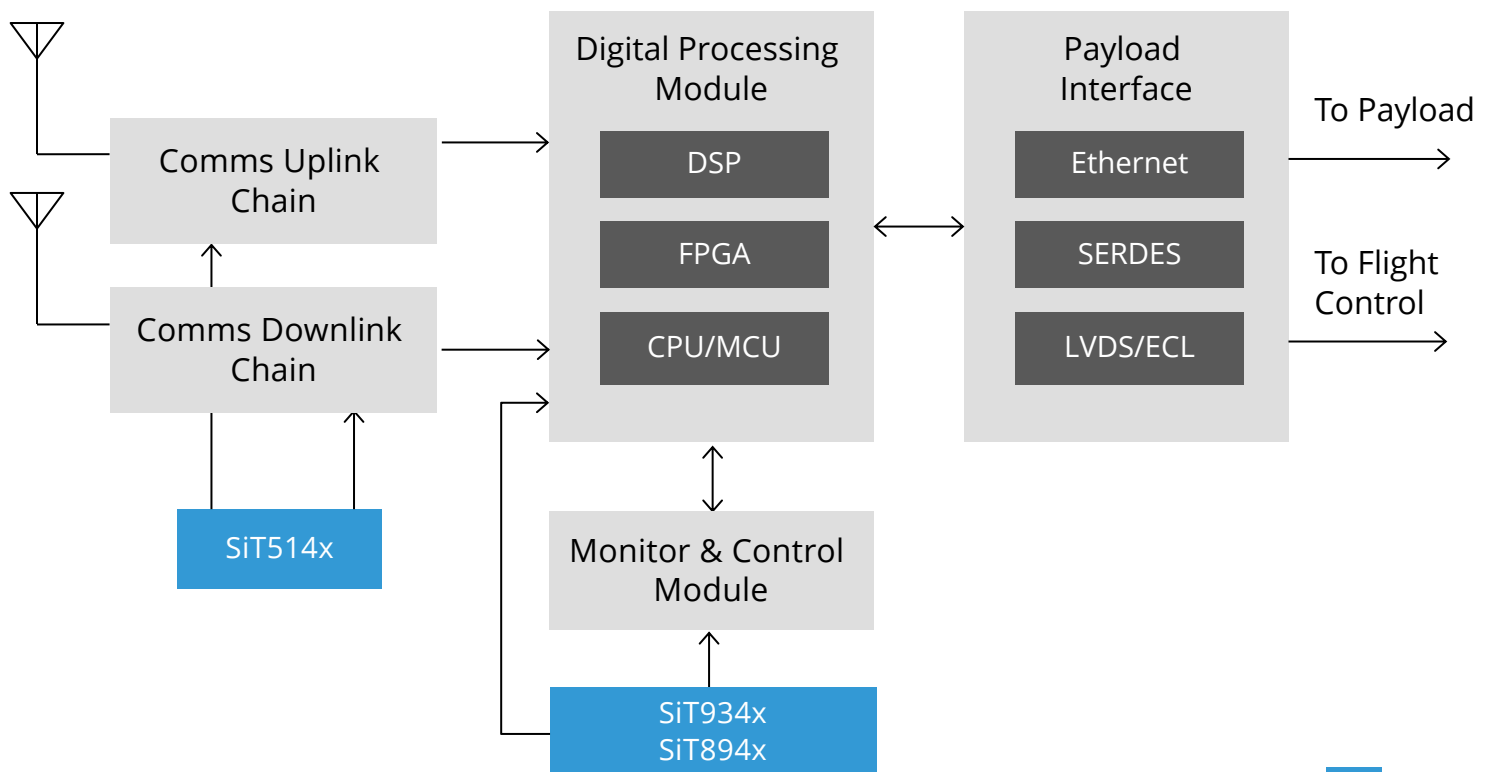
- Conforms to MIL-PRF-55310
- No quartz reliability issues
- No cover or shielding

With closer proximity to earth, low earth orbit (LEO) satellites provide lower latency and higher bandwidth compared to a geostationary satellite. LEO satellites offer efficient data transmission with high data transfer rates, stronger signal strength, and lower signal attenuation.

To achieve this, they depend on ruggedized reference clocks for uplink/downlink comms and clocking the processor. The timing components must withstand high levels of vibration during launch and continued extreme temperature variations in orbit. If timing fails, receivers lose lock to satellites and navigation systems won't receive critical data.

SiTime ruggedized Endura™ timing solutions are built to operate in harsh environments, overcoming the inherent weaknesses of quartz and delivering low failure rates, long-term reliability, and robust performance.

Block Diagram



Type	Product	Frequency	Key Features	Key Values
Super-TCXOs	<a href="#">SiT5146</a>	1 to 60 MHz	<ul style="list-style-type: none"> <li>• <math>\pm 0.5</math> to <math>\pm 2.5</math> ppm stability</li> <li>• 0.004 ppb/g, <math>\pm 15</math> ppb/<math>^{\circ}\text{C}</math></li> </ul>	<ul style="list-style-type: none"> <li>• Reference clock for uplink/downlink comms</li> </ul>
	<a href="#">SiT5147</a>	60 to 220 MHz		
Differential XOs	<a href="#">SiT9346</a>	1 to 220 MHz	<ul style="list-style-type: none"> <li>• <math>\pm 10</math> to <math>\pm 50</math> ppm stability</li> <li>• 0.1 ps RMS phase jitter</li> </ul>	<ul style="list-style-type: none"> <li>• DSP, FPGA &amp; processor clocking</li> </ul>
	<a href="#">SiT9347</a>	220 to 725 MHz		
LVCMOS XOs	<a href="#">SiT8944</a>	1 to 110 MHz	<ul style="list-style-type: none"> <li>• <math>\pm 20</math> to <math>\pm 50</math> ppm stability</li> <li>• <math>-55^{\circ}\text{C}</math> to <math>125^{\circ}\text{C}</math></li> </ul>	<ul style="list-style-type: none"> <li>• DSP, FPGA &amp; processor clocking</li> </ul>
	<a href="#">SiT8945</a>	115 to 137 MHz		



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