

SiTime MEMS timing benefits

Complete MEMS clock tree

- Precision MEMS TCXO
- Stratum 3E MEMS OCXO
- MEMS clock IC/PLL

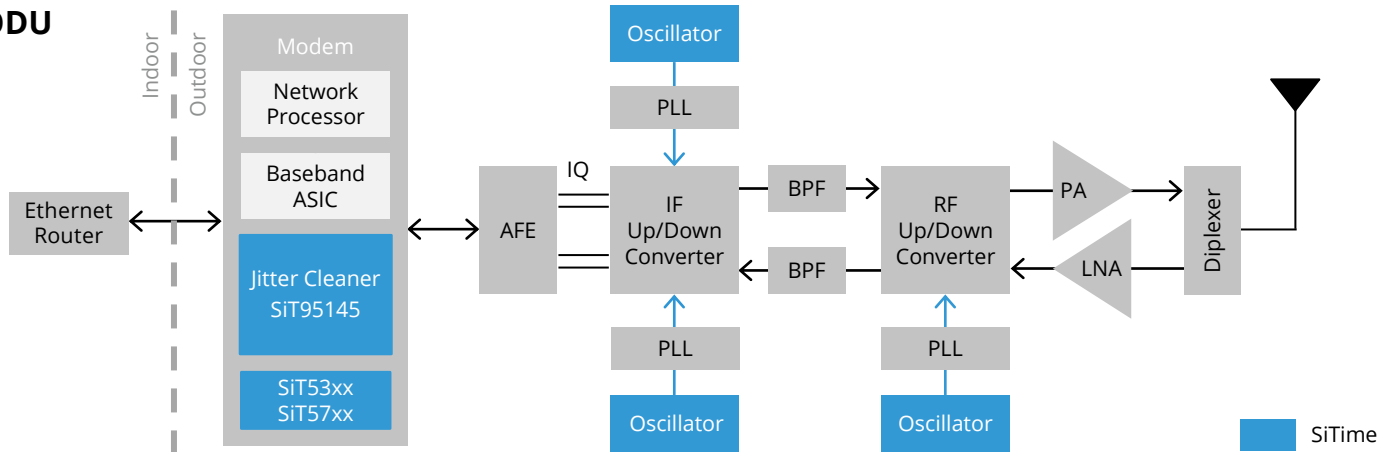
Most Robust in real world conditions

- 4x better dF/dT, airflow and heat resistant
- 20x better vibration resistance
- Immunity to power supply noise

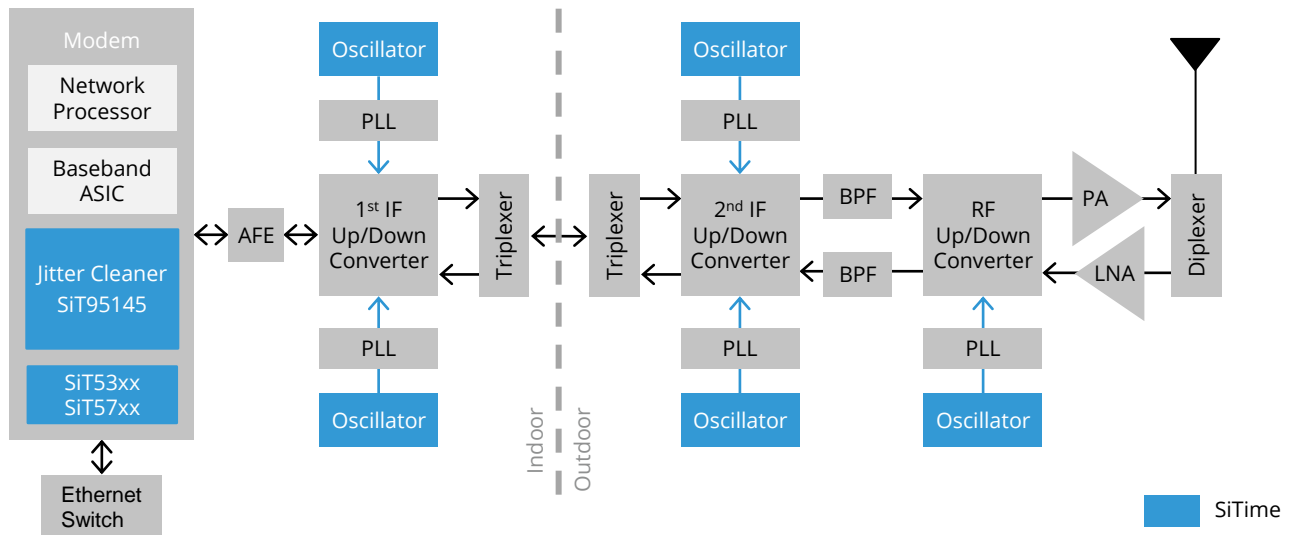
Integrated MEMS, easy to use

- No external quartz
- No quartz reliability issues
- No cover or shielding

Full ODU

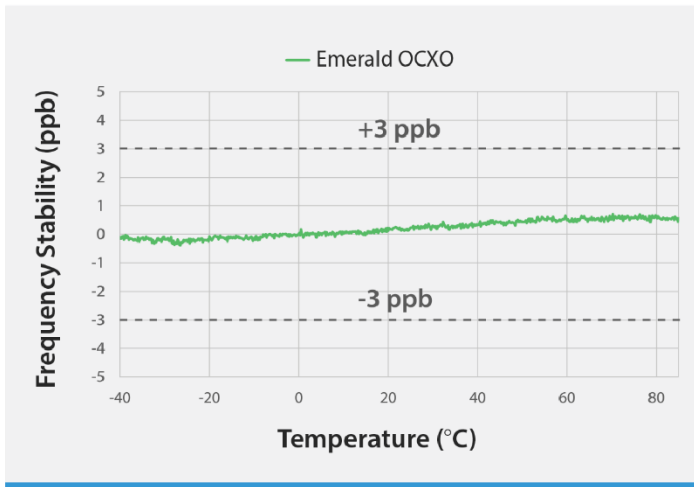


Split-mount ODU

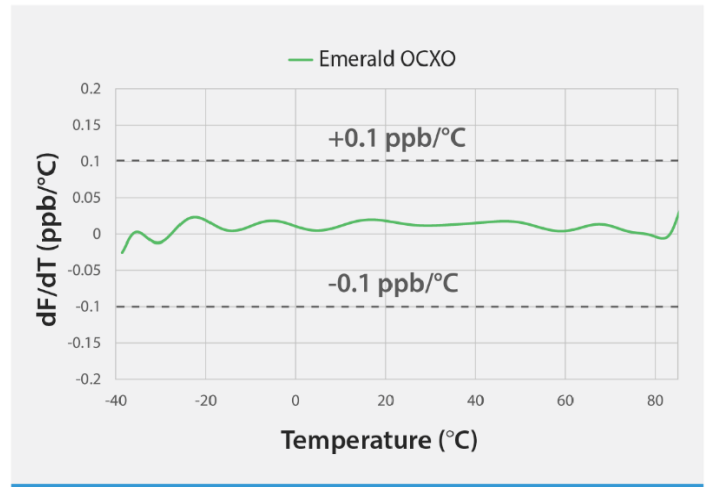


| Application | Devices | Type | Function | Key Features |
|-------------------------|--------------------------|--------------------------------------|---|---|
| ODU baseband processing | SiT535x | Super-TCXOs | Reference clock for jitter cleaner and IEEE1588 | 1 to 220 MHz, ± 100 ppb, ± 1 ppb/ $^{\circ}$ C 105 $^{\circ}$ C |
| | SiT57xx | OCXOs | | 1 to 60 MHz, ± 5 ppb, ± 0.04 ppb/ $^{\circ}$ C |
| | SiT9514x | Network synchronizer, Jitter cleaner | Ethernet, PCIe, processor & IEEE1588 clocking | 4-in, 11-out, 4-PLL, 8 kHz to 2.1 GHz |

Better Stability



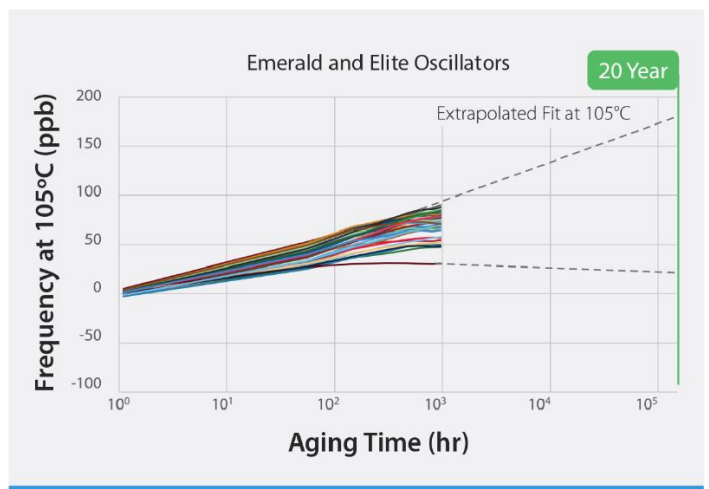
Better Frequency Slope



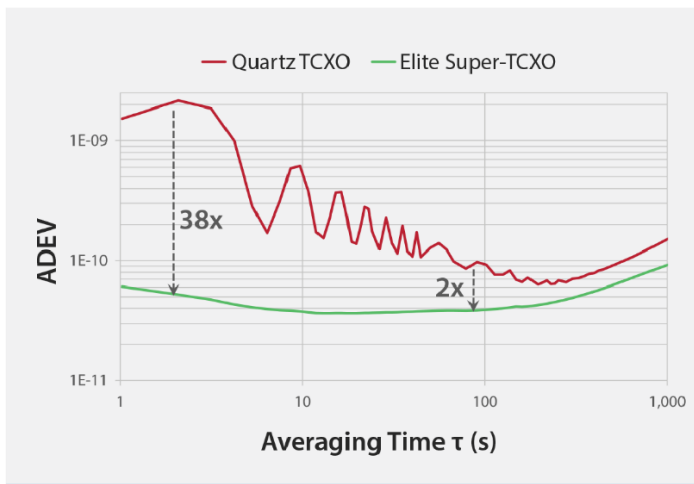
Better Vibration Resistance



Better Aging



Better Allan Deviation



Better PSNR (Power Supply Noise Rejection)

