

SiTime MEMS timing benefits

Complete MEMS clock tree

- Precision MEMS TCXO
- Stratum 3E MEMS OCXO
- MEMS clock IC/PLL

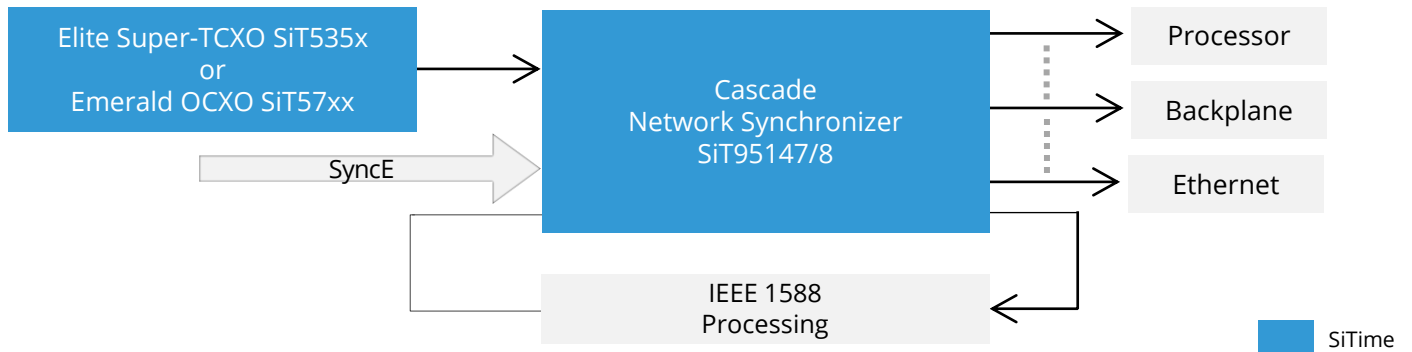
Most robust in real world conditions

- 4x better dF/dT for accurate IEEE 1588
- Resistant to airflow, heat, vibration
- Smart clock monitoring and hit switching for redundancy

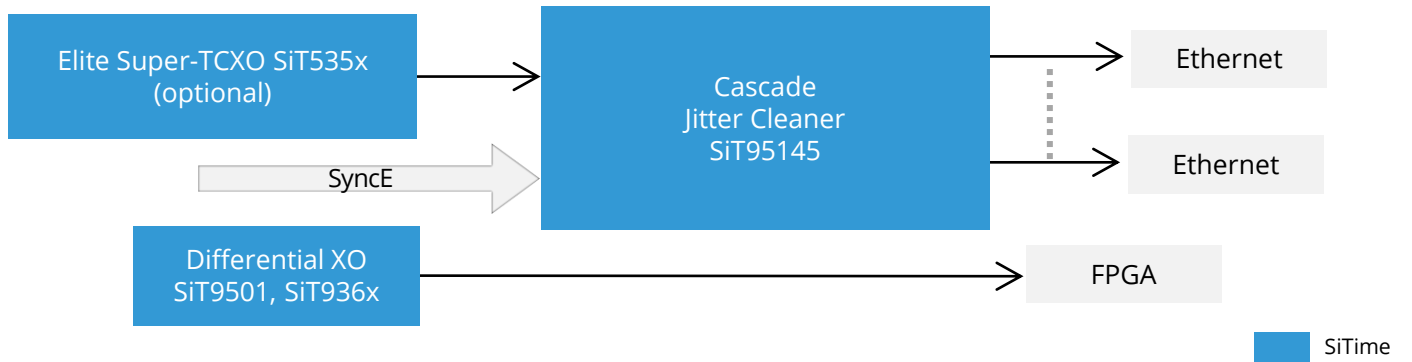
Integrated MEMS, easy to use

- No external quartz
- No quartz reliability issues
- No cover or shielding needed

Top-of-the-Rack or Chassis (Timing Card) Based Switch

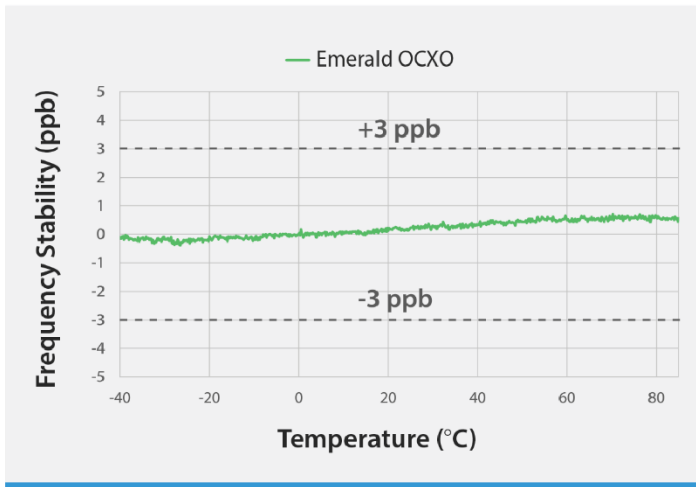


Line Card

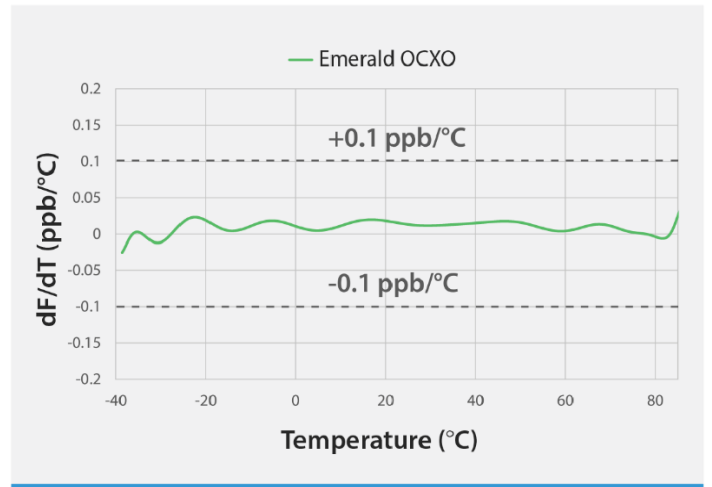


Application	Devices	Type	Function	Key Features
Fronthaul or IP RAN switch	SiT535x	Super-TCXO	Reference clock for jitter cleaner and IEEE 1588	1 to 220 MHz, ± 100 ppb, ± 1 ppb/ $^{\circ}$ C 105 $^{\circ}$ C
	SiT57xx	OCXO		1 to 60 MHz, ± 5 ppb, ± 0.04 ppb/ $^{\circ}$ C
	SiT9514x	Network synchronizer, jitter cleaner	Ethernet, processor	4-in, 11-out, 4-PLL, 8 kHz to 2.1GHz
	SiT9501 SiT936x	Differential XO	Ethernet, FPGA	1 to 725 MHz, 0.07 to 0.25 ps jitter, 105 $^{\circ}$ C

Better Stability



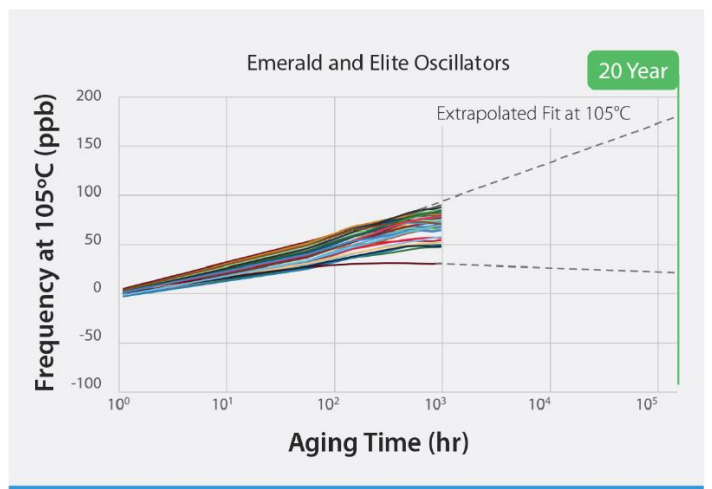
Better Frequency Slope



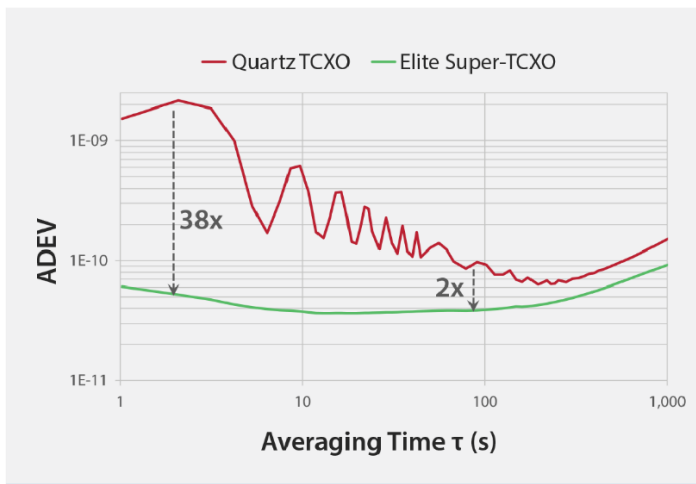
Better Vibration Resistance



Better Aging



Better Allan Deviation



Better PSNR (Power Supply Noise Rejection)

