

SiTime MEMS timing benefits

Complete MEMS clock tree

- Precision MEMS TCXO
- Low jitter differential MEMS XO

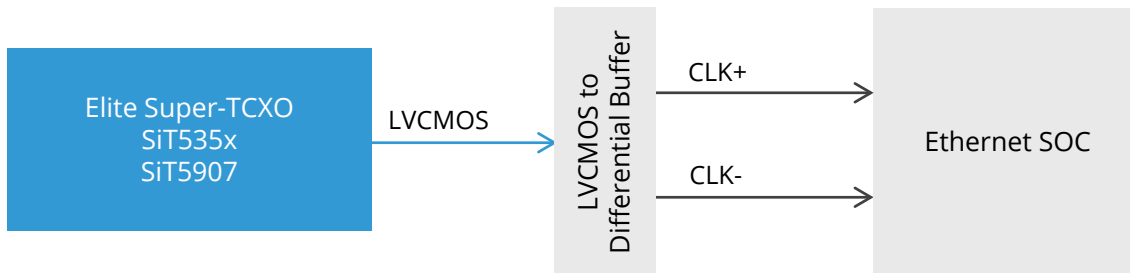
Most Robust in real world conditions

- 4x better dF/dT for accurate IEEE 1588
- Resistant to airflow and heat
- Immunity to power supply noise

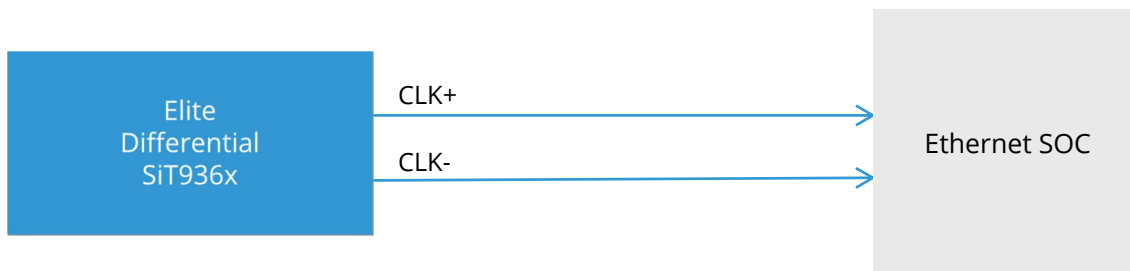
Thin profile, easy to use

- No cover or shielding
- ≤1 mm thin that fits back side of PCIe card

IEEE1588 Enabled, 10/40/100GbE NIC

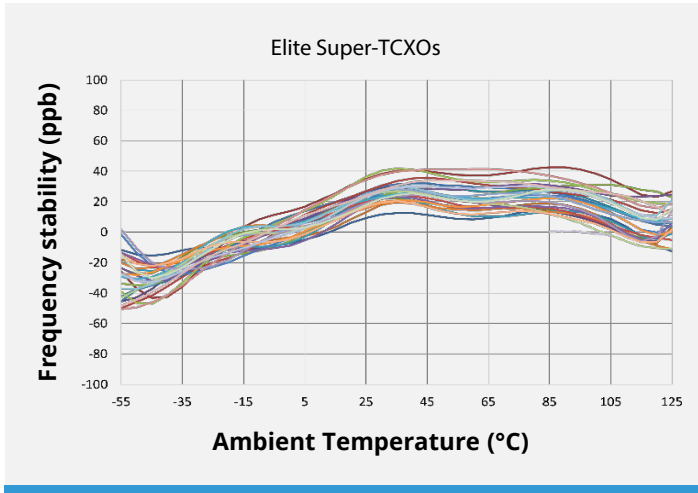


10/40/100GbE NIC

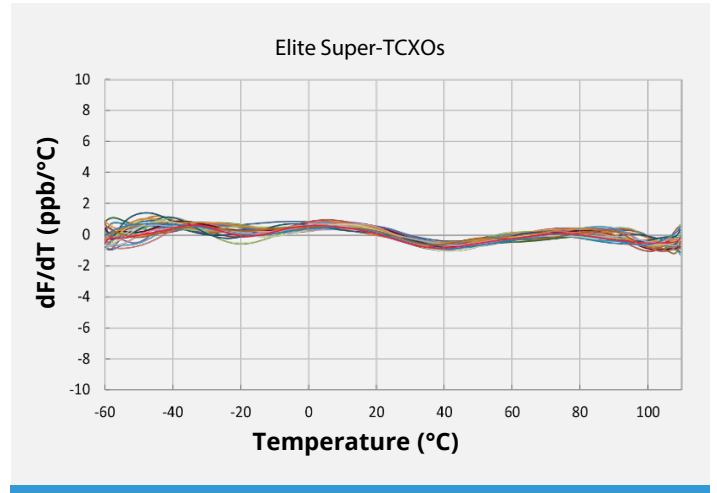


Application	Devices	Type	Function	Key Features
Intel Fortville NIC	SiT5907	Super-TCXO	IEEE1588 and high-speed SERDES reference clock	20 MHz, ±20 ppb up to 70°C, operable to 105°C
Other Smart NIC	SiT535x	Super-TCXO		1 to 220 MHz, ±100 ppb, ±1 ppb/°C 105°C
	SiT936x	Differential XO	High-speed SERDES reference clock	1 to 725 MHz, 0.1 ps jitter for Ethernet, 105°C

Better Stability



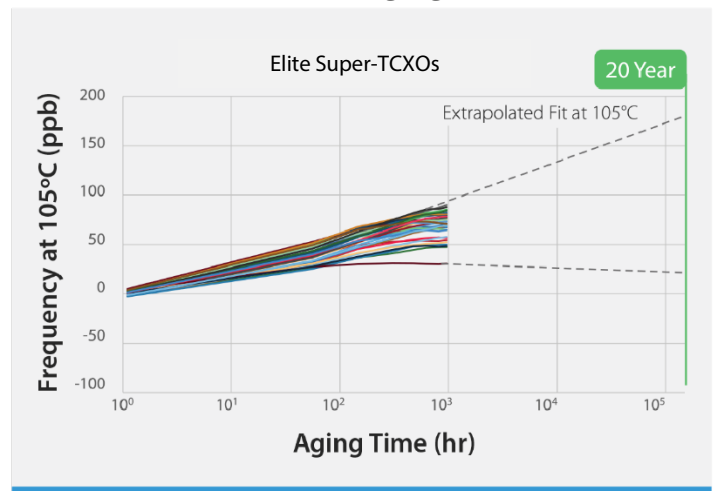
Better Frequency Slope



Better Vibration Resistance



Better Aging



Better Allan Deviation



Better PSNR (Power Supply Noise Rejection)

