

Features

- World's thinnest, high accuracy programmable oscillator: 0.37mm typical
- Factory programmable: 1 MHz to 125 MHz
- Standby or Output Enable low power modes
- Can be embedded inside standard IC packages!
- Outstanding mechanical robustness



SiT8002UT Applications:

- Cellphones, Portable A/V Players, HDD, DSC/DVC
- Multi-Chip Modules (MCM)

SiT8002UT Supported Protocols:

- USB 2.0, PATA, Ethernet

How to Order - Part Number Guide

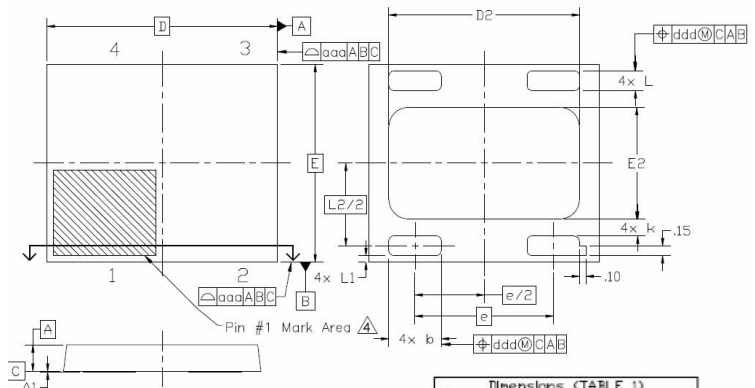
SiT8002AX-5X-18X-FFF.FFFFFT

Temperature Range	Frequency Tolerance:	Optional Pin Function:	Frequency (1 - 125 MHz):	Package:
"C" -10 to 70°C "I" -40 to 85°C	"6" +/- 200 ppm "7" +/- 500 ppm	"E" is Output Enable "S" is Standby	e.g. 14.31818 e.g. 125.000	"T" is Tape & Reel

Specifications

Parameter	Condition	Min	Max	Unit
Output Frequency Range		1	125	MHz
Frequency Tolerance	"6" over temperature, supply voltage, aging		+/-200	ppm
	"7" over temperature, supply voltage, aging		+/-500	ppm
Operating Temperature Range	"C"	-10	70	°C
	"I"	-40	85	°C
Supply Voltage		1.7	1.9	V
Current Consumption	Under 3pF load @ 12 MHz, T=25 °C		13	mA
Standby Current	Output is Weakly Pulled Down		50	µA
Duty Cycle		40	60	%
Rise/Fall Time			2	ns
Output Voltage-"L"			30	%VDD
Output Voltage-"H"			70	%VDD
Output Load			15	pF
Start Up Time	T=25 °C		20	ms
Vdd Ramp Time		0	200	ms

Dimensions



Symbol	Value	Note
aaa	0.05	
ccc	0.0500	
ddd	0.10	

Symbol/ Var	MIN	NOM	MAX
A	0.30	--	0.40
A1	-0.0000	--	0.10
k	0.75	0.88	0.9500
l	0.20	0.25	0.30
B BSC		3.50	
E BSC		3.20	
DE	0.95	0.95	0.95
EP	1.65	1.70	1.75
e BSC		0.10	
L	0.25	0.30	0.35
L1	0.00	--	0.20
L1 BSC		0.5000	
N (# of Lands)			4

Pin Number	Pin Name	Pin Type	Pin Description
1	OE/ST	Digital In	Output Enable, Standby
2	GND	Power	Connect to Ground
3	CLK	Digital Out	Clock Output
4	VDD	Power	Connect +1.8V
Exposed Pad	EP	Power	Connect to Ground